U.S. Patent and Trademark Office PTOL-37 (Rev. 7-05)

of Biological Material

4. Examiner's Comment Regarding Requirement for Deposit

9. Other ____.

8. X Examiner's Statement of Reasons for Allowance

DETAILED ACTION

Response to Amendment

This office action is in response to the amendment filed September 28, 2005.

Accordingly, claims 1-45 were canceled and new claims 46-61 are added.

Currently, claims 46-61 are pending.

Allowable Subject Matter

Claims 46-61 are allowed.

The following is an examiner's statement of reasons for allowance: none of the prior art of record, taken alone or in combination, fairly shows or suggests all the limitations as claimed.

Re claim 46, none of the prior art of record discloses a method for forming a rectifying junction on an alloy-semiconductor material comprising a compound comprising a first component and a second component, the method comprising: photo-electrochemical removal of the first component of a first portion of the alloy-semiconductor material to form an N-type region enriched in the second component; and substantially removing the second component from a second portion of the alloy-semiconductor material to form a P-type region enriched in the first component.

Re claim 51, none of the prior art of record discloses a method for forming a rectifying junction on material, the method comprising the steps of: providing the alloy-semiconductor material having at least a first element and a second element, wherein the alloy-semiconductor has a first region and a second an alloy-semiconductor region substantially separated by an intermediate region; substantially removing the first element from the first region, wherein the removal of the first element from the first region substantially

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enriched material of the second element, and wherein the removal of the first element from the first region substantially forms a negatively doped material in the first region to act as an N-type region; substantially removing the second element of the second region, wherein the removal of the second element of the second region forms a positively doped material in the second region to act as a P-type region; and providing a contact on the P-type region, wherein the N-type region, the intermediate region and the P-type region with the contact substantially form a P-I-N rectifying junction.

Re claim 57, none of the prior art of record discloses a method for forming a rectifying junction on an alloy-semiconductor, the method comprising the steps of: applying an N-type material on a first region of the alloy-semiconductor; heating the N-type material on the alloy-semiconductor; photo-electrochemical etching the N-type material; substantially covering the N-type material for protection; chemically etching a second region of the alloy-semiconductor to form P-type material; and applying a contact to the P-type material to form a P-I-N rectifying junction.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. The following U.S. Patents teaches the use of photo-electro-chemical etching in semiconductor processing: Wang '981, Propst '627, Peeters '189 and Kohl '442. However, none

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of the prior art of record, taken alone or in combination, fairly shows or suggests all the limitations as claimed.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Khanh B. Duong whose telephone number is (571) 272-1836. The examiner can normally be reached on 10:00-6:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Zandra Smith can be reached on (571) 272-2429. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

KRD

YANDRAV.SMITH PRIMARY EXAMINER